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Application Number 10727,106
Filing Date December 3, 2003
First Named Inventor Kiehl, et al.
Art Unit 2846 2819
Examiner Name JBD J. Cho
Attorney Docket Number 2003 P 52929 US

U.S. PATENT DOCUMENTS

Examiner Initials*	Cite No.	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
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FOREIGN PATENT DOCUMENTS

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JL	A	P.R. GRAY, et al., "Analysis and Design of Analog Integrated Circuits" ISBN 0-471-87493-0, 1984 Second Edition, pp. 59-67, John Wiley & Sons, Inc., New York	
		No mention	

Examiner Signature	James Cho	Date Considered	6/23/05
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